

PECL COMPATIBLE CRYSTAL OSCILLATOR IN CERAMIC LCC PACKAGE - XO75P1 Series

FEATURES

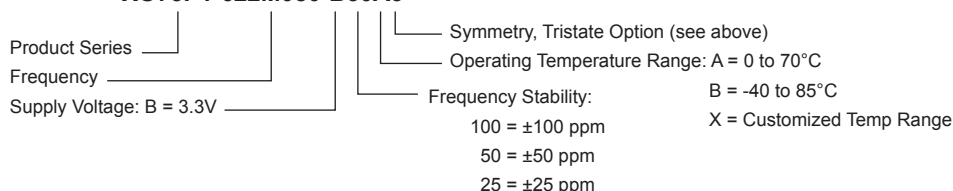
- RoHS Compliant (Pb-Free), Best Suited for ECL Logic Devices
- Low Phase Jitter with New Generation PLL Design
- Leadless Chip Carrier (LCC) Ultra Small Package, Industry de facto Standard Footprint
- Complimentary Output, Tri-state Enable/Disable Standard

SPECIFICATIONS

Frequency Range	300 MHz to 700 MHz
Standard Frequency	312.500MHz, 622.080 MHz
Input Voltage (Vcc)	B = +3.3 VDC \pm 5%
Input Current	100 mA Maximum
Storage Temperature	-55°C to 125°C
Overall Frequency Stability	100 = \pm 100 ppm; 50 = \pm 50 ppm; 25 = \pm 25 ppm
Temperature Range	A = 0°C to 70°C; B = -40°C to 85°C
Standard Stability	50A = \pm 50 ppm / 0°C to 70°C
Duty Cycle	3 = Tristate 55/45% symmetry
Output Load	50 Ohms to Vcc - 2V or Thevenin Equivalent, Bias Required
Logic "1" / Logic "0" Level	(Vcc - 1.02V) Minimum / (Vcc - 1.63V) Maximum
Rise/Fall Time (Tr/Tf)	1 ns Maximum at 20% to 80% Vp-p
Start-up time	5 ms Maximum
Phase Jitter (RMS, 1 Sigma)	1.6 ps Typical for fj = 12KHz to 20MHz, at 622.080MHz
Tristate Function	Input (Pin 1) High (> 2.2V) or open: Output (Pin 4, 5) active Input (Pin 1) Low (< 0.4V): Output disabled in high impedance
Enable/Disable Time	100 ns Maximum

Creating a Part Number

XO75P1-622M080-B50A3



OUTLINE DRAWING

